



*Trace*

*2812*

501.35437CV2

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: M. YOSHIDA et al.  
Application No.: 09/416,959  
Filed: October 13, 1999  
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND  
PROCESS FOR MANUFACTURING THE SAME  
Art Unit: 2812  
Examiner: R. Pompey

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

September 25, 2003

Sir:

In response to the Office Action dated June 30, 2003, please amend the  
above-identified application as listed below and as set forth on the following pages:

**Amendments to the claims,**

**Remarks are included following the amendments.**